



NETL.001DV2

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants	:	Jayesh R. Bhakta et al.	Group Art Unit 2818
Appl. No.	:	10/674,082	
Filed	:	September 29, 2003	
For	:	ARRANGEMENT OF INTEGRATED CIRCUITS IN A MEMORY MODULE	
Examiner	:	Ly D. Pham	

**RESPONSE TO AUGUST 18, 2004 OFFICE ACTION**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the August 24, 2004 Office Action, Applicants request the Examiner to reconsider the above-identified patent application in view of the following remarks.

**Remarks:** Begin on page 2 of this Response.